

AMENDMENTS TO CLAIMS

This listing of claims will replace all prior versions, and listings, of claims in the application:

Listing of Claims:

Claim 1. (currently amended) A high-resolution still picture decoding device, comprising:

a memory device having a bit-stream buffer, a temporary buffer and a frame buffer, the bit-stream buffer being adapted to store bit-stream data from a storage media; and

a decoding means for decoding the bit-stream data in the bit-stream buffer and storing decoded frame data in the frame buffer or the temporary buffer, such that, when a still picture is to be displayed, the frame buffer stores a part of the frame data that has been decoded from a part of the bit-stream data and corresponds to the still picture, and the temporary buffer is provided to store the other frame data from the other part of the bit-stream data which has not been decoded and which is to be decoded in real time as the still picture is being displayed, and the decoded frame data in the frame buffer and the real-time decoded frame data from the temporary buffer are output for displaying a high-resolution still picture,

wherein the frame data ~~in~~ from the bit-stream buffer is represented by multiple macroblock lines after being decoded by the decoding means, the frame buffer only stores a portion of the macroblock line that has been decoded and corresponds to the frame data of the still picture, and the temporary buffer has a memory space for storing at least one macroblock line ~~to be decoded~~ for decoding in real time as the still picture is being displayed.

Claim 2. (previously presented) The high-resolution still picture decoding device as claimed in claim 1, wherein the frame buffer stores only a half of the frame data ~~corresponding that has been~~ decoded and corresponds to the picture, and the temporary buffer has a memory space less than that of the frame buffer.

Claims 3 and 4 (canceled)

Claim 5. (Previously presented) The high-resolution still picture decoding device as claimed in claim 1, wherein the frame buffer stores even numbered macroblock lines that have been decoded and correspond to the frame data of the picture.

Claim 6. (currently amended) The high-resolution still picture decoding device as claimed in claim 5, wherein the macroblock lines ~~to be~~ decoded in the temporary buffer have a height half of the height of the macroblock lines in the frame buffer that have been decoded.

Claim 7. (currently amended) The high-resolution still picture decoding device as claimed in claim 5, wherein, for a field of a displaying still picture and in a time point of switching to display the still picture, the bit-stream of a frame from the bit-stream buffer is processed by a first decoding process in one field display time of a top ~~field~~ field to decode the even numbered macroblock lines, and the decoded frame data is stored in the frame buffer, while in one field display time of the other fields, the bit-stream of a frame from the bit-stream buffer is processed by a second decoding process to decode the odd numbered macroblock lines.

Claim 8. (previously presented) The high-resolution still picture decoding device as claimed in claim 7, wherein the second decoding process is repeatedly executed and there are two pointers for setting a picture range in which read operations of bit-stream data are cyclically repeated.

Claim 9. (original) The high-resolution still picture decoding device as claimed in claim 8, wherein the decoding means has an image decoder which comprises a variable-length decoder, a run-length decoder, an inverse quantizer and an inverse discrete cosine transform unit, and data read out from bit-stream buffer is processed by the variable-length decoder, run-length decoder, inverse quantizer, and inverse discrete cosine transform unit sequentially for being stored in frame buffer or temporary buffer.

Claim 10. (previously presented) The high-resolution still picture decoding device as claimed in claim 9, wherein the variable-length decoder discards unnecessary macroblock lines for decoding.

Claim 11. (original) The high-resolution still picture decoding device as claimed in claim 9, wherein the image decoder further comprises a DC predictor for preserving DC component of the bit-stream data processed by the variable-length decoder.

Claim 12. (original) The high-resolution still picture decoding device as claimed in claim 9, wherein, only data of one field is determined by the inverse discrete cosine transform for being stored into the temporary buffer.

Claim 13. (currently amended) The high-resolution still picture decoding device as claimed in claim 1, further comprising a multiplexer which ~~select~~ selects the decoded frame data in the frame buffer or the real-time decoded framed data from the temporary buffer to output.